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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:  
JI MYONG LEE

Confirmation No: 9617

Application No.: 10/743,454

Group Art Unit: 2823

Filed: December 23, 2003

Examiner: Estrada, Michelle

Title: METHOD FOR PLANARIZING A SURFACE OF A SEMICONDUCTOR WAFER

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION  
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